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				Applicati n / Conf. N .	Unknown /
				Filing Date	September 11, 2003
				First Named Invent r	Eric D. Groen
				Art Unit	Unknown
				Examiner Name	Unknown
				Attorney Docket Number	X-1368 US
Sheet	1	of	2		

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Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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OTHER -- NON PATENT LITERATURE DOCUMENTS					
Examiner Initials *	Cite No[†]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T *		
		Xilinx, Inc.; "Aurora Protocol Specification"; SP002 (v1.1) , February 14, 2003; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124 or www.xilinx.com; pp. 1-63.			
		Xilinx, Inc., "Using FPGAs to Design Gigabit Serial Backplanes"; Programmable World 2002, The Forum for the New Era of Systems Design; April 17, 2002; pp. A1-2 through A1-34.			

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